

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	<b>Application Number</b>		10629228
	<b>Filing Date</b>		2003-07-28
	<b>First Named Inventor</b>	Lewis B. Aronson	
	<b>Art Unit</b>		2613
	<b>Examiner Name</b>	Kenneth J. Malkowski	
<b>Attorney Docket Number</b>		15436.247.2.1.5	

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1	GREEN, MICHAEL, et al., "Mixing it up with OC-192 CMOS Transceivers", CommsDesign, An EE Times Community, 8 pgs, 03 May 2006, available at: <a href="http://www.commsdesign.com/main/2006/12/0012/estl6.htm">http://www.commsdesign.com/main/2006/12/0012/estl6.htm</a>	<input type="checkbox"/>
2	Dallas Semiconductor, "HFTA-01.0: 2.5Gbps SDH/SONET Fiber Optic Chipset," 4 pages October 10, 2000, available at: <a href="http://www.maxim-ic.com/appnotes.cfm/appnote_number/713/1n/en">http://www.maxim-ic.com/appnotes.cfm/appnote_number/713/1n/en</a>	<input type="checkbox"/>
3	Dallas Semiconductor, "MAX3872 Multirate Clock and Data Recovery with Limiting Amplifier," 2 pages, March 30, 2003, available at: <a href="http://www.maxim-ic.com/quick_view2.cfm/qv_pk/3705">http://www.maxim-ic.com/quick_view2.cfm/qv_pk/3705</a>	<input type="checkbox"/>
4	Huiqing Zhong and Stephen I. Long, "Monolithic Clock and Data Recovery Chip for 10GB/S Fiber Communications Systems," 4 pages 1997-98.	<input type="checkbox"/>
5	GIGA an Intel Company, "2.5 Gbit/s Clock and Data Recovery GD16546B," 9 pages, July 31, 2001.	<input type="checkbox"/>

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Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

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- ☐ See attached certification statement.
- ☒ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☐ None

**SIGNATURE**

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Peter F. Malen, Jr./	Date (YYYY-MM-DD)	2007-06-29
Name/Print	Peter F. Malen, Jr.	Registration Number	45576

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